## Notice of References Cited

Application/Control No. 09/939,767	Reexamination	Applicant(s)/Patent Under Reexamination YAMAZAKI, SHUNPEI	
Examiner	Art Unit		
David L. Hogans	2813	Page 1 of 1	

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	В	US-			
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	U	Wolf et al., Silicon Processing for the VLSI Era (1990), Lattice Press, Volume 2, page 186.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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